

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. SAKATA, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 10/103,966, filed March 25, 2002

Filed: On even date herewith

For: SEMICONDUCTOR INTEGRATED CIRCUITS WITH
POWER REDUCTION MECHANISM

Art Group of Parent: 2819

Examiner of Parent: D. D. Chang

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 25, 2003

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE SPECIFICATION:

On page 1, before the heading "BACKGROUND OF THE INVENTION," please insert the following heading and paragraph:

"CROSS-REFERENCE TO RELATED APPLICATIONS"

This is a Continuation of application Serial No. 10/103,966, filed March 25, 2002, which is a divisional of application Serial No. 09/832,853, filed April 12, 2001; which is a continuation of Serial No. 09/573,609, filed May 19, 2000 (now USP 6,268,741); which is a continuation of application Serial No. 09/141,563, filed on

August 28, 1998 (now USP 6,154,062); which is a continuation of application Serial No. 08/797,051, filed on February 10, 1997 (now USP 5,825,1998); which is a continuation of application Serial No. 08/620,868, filed March 21, 1996 (now USP 5,606,265); which is a continuation of application Serial No. 08/374,990, filed on January 19, 1995 (now USP 5,521,527); which is a continuation of application Serial No. 08/178,020, filed on January 6, 1994 (now USP 5,408,144), the entire disclosures of which are hereby incorporated by reference.